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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/998,830	12/03/2001	Taiyuu Miyamoto	027260-504	7164

7590 02/17/2005

Platon N. Mandros
BURNS, DOANE, SWECKER & MATHIS, L.L.P.
P.O. Box 1404
Alexandria, VA 22313-1404

EXAMINER

RIZZUTO, KEVIN P

ART UNIT	PAPER NUMBER
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2183

DATE MAILED: 02/17/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/998,830

Applicant(s)

MIYAMOTO, TAIYUU

Examiner

Kevin P Rizzuto

Art Unit

2183

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 12/03/01, 3/26/03, 6/24/04 and 11/23/04.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-8 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☐ Claim(s) _____ is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☒ Claim(s) 1-8 are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

1. Claims 1-8 are presented for examination.

Election/Restrictions

2. Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - I. Claims 1, 2, 5, 6, and 8 are drawn to hardware to prevent external access or commands to change modes or data of a processor via an externally input data, classified in class 712, subclass 39.
 - II. Claims 3 and 4 are drawn to memory map selection hardware, classified in class 711, subclass 100.
 - III. Claim 7 is drawn to a voltage regulating system to prevent spurious signals, classified in class 713, subclass 340.

Inventions I, II and III would require divergent searches because they are distinct inventions as described below and because they have different classifications as shown above.

3. The inventions are distinct, each from the other because of the following reasons:

Inventions I, II and III are related as sub-combinations disclosed as usable together in a single combination. The sub-combinations are distinct from each other if they are shown to be separately usable.

In the instant case, invention I has separate utility such as hardware for decoding a code that signals whether or not to allow certain commands through to the

processor or prevents external access to the processor to change modes. The microprocessor with the external mode change protection and command limiting does not need the memory map selection or voltage regulator to operate in order for invention I to work properly with the microprocessor and therefore is separately usable. As disclosed in the applicant's admitted prior art, the microprocessor functions without inventions II or III.

Invention II has a separate utility such as for implementing a memory map selector for choosing different memory maps based on a decoded value. The microprocessor can use the memory map selection hardware as disclosed for invention II without the voltage regulator or the external mode altering prevention and command limiting from the other inventions, and therefore is separately usable. As disclosed in the applicant's admitted prior art, the microprocessor functions without inventions I or III.

Invention III has a separate utility such as regulating voltage by monitoring the power supply and preventing spurious writes if the power supply drops. The microprocessor can use the voltage regulator as disclosed for invention III without the memory map selection hardware or the external mode and command limiting from the other inventions, and therefore is separately usable. As disclosed in the applicant's admitted prior art, the microprocessor functions without inventions I or II.

In summary, any one invention (Group I, II or III) may be added to applicant's prior art to implement the individual invention's disclosed purpose without necessarily adding the other inventions.

4. Because these inventions are distinct for the reasons given above and would require divergent searches and have acquired a separate status in the art as shown by their different classification thus putting a serious burden on the examiner, restriction for examination purposes as indicated is proper.

5. Applicant is advised that the response to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR § 1.143).

6. A telephone call was made to Platon Mandros on 2/15/2005 to request an oral election to the above restriction requirement, but did not result in an election being made.

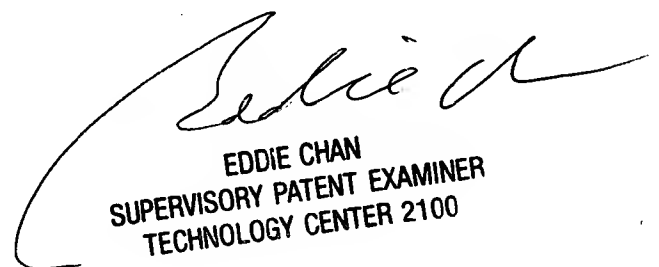
Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin P Rizzuto whose telephone number is (703) 305-6783. The examiner can normally be reached on M-F, 8-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Chan can be reached on (703) 305-9712. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



EDDIE CHAN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100